

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims

- First step*
1. (Original) A method for processing a semiconductor topography, comprising polishing the topography on a polishing pad without depositing water on the polishing pad.
 2. (Original) The method of claim 1, wherein a pH of a polishing solution on the polishing pad is substantially uniform during the polishing.
 3. (Original) The method of claim 1, wherein a pH of a polishing solution on the polishing pad varies by less than about 2.5 during the polishing.
 4. (Original) The method of claim 1, wherein a pH of a polishing solution on the polishing pad varies by less than about 30 % during the polishing.
 5. (Original) The method of claim 1, wherein subsequent to the polishing, a substantial amount of residual slurry particles are present on the topography.
 6. (Original) The method of claim 1, wherein subsequent to the polishing, the topography is substantially free of agglomerated slurry particles.
 7. (Original) The method of claim 1, wherein subsequent to the polishing, the topography is substantially free of slurry particles having a particle size of greater than about 10 μm .
 8. - 12. (Canceled)
 13. (Original) A method for processing a semiconductor topography, comprising depositing water on a polishing pad in a plurality of dispense intervals during polishing of the topography to reduce a rate of change of a pH of a polishing solution on the topography.
 14. (Original) The method of claim 13, wherein each of the plurality of dispense intervals comprise a dispense time of less than about 30 seconds.
- second step*

15. (Original) The method of claim 13, wherein one or more of the plurality of dispense intervals comprise a dispense time of less than about 3 seconds.

16. (Original) The method of claim 13, wherein the polishing solution comprises slurry present on the topography prior to the polishing.

17. (Original) The method of claim 13, wherein additional polishing solution is not deposited on the polishing pad during the polishing.

18. (Original) The method of claim 13, wherein the topography comprises an upper layer of oxide formed across the topography, and wherein the oxide is substantially planar subsequent to the polishing.

19. (Original) A method for processing a semiconductor topography, comprising:

polishing the topography on a primary polishing pad during a primary polishing step without depositing water on the primary polishing pad; and

polishing the topography on a final polishing pad during a final polishing step, comprising depositing water on the final polishing pad in a plurality of dispense intervals to reduce a rate of change of a pH of a polishing solution on the topography.

20. (Original) The method of claim 19, further comprising transferring the topography from the primary polishing pad to the final polishing pad subsequent to the primary polishing step, wherein a substantial amount of residual slurry particles are present on the topography during the transferring.